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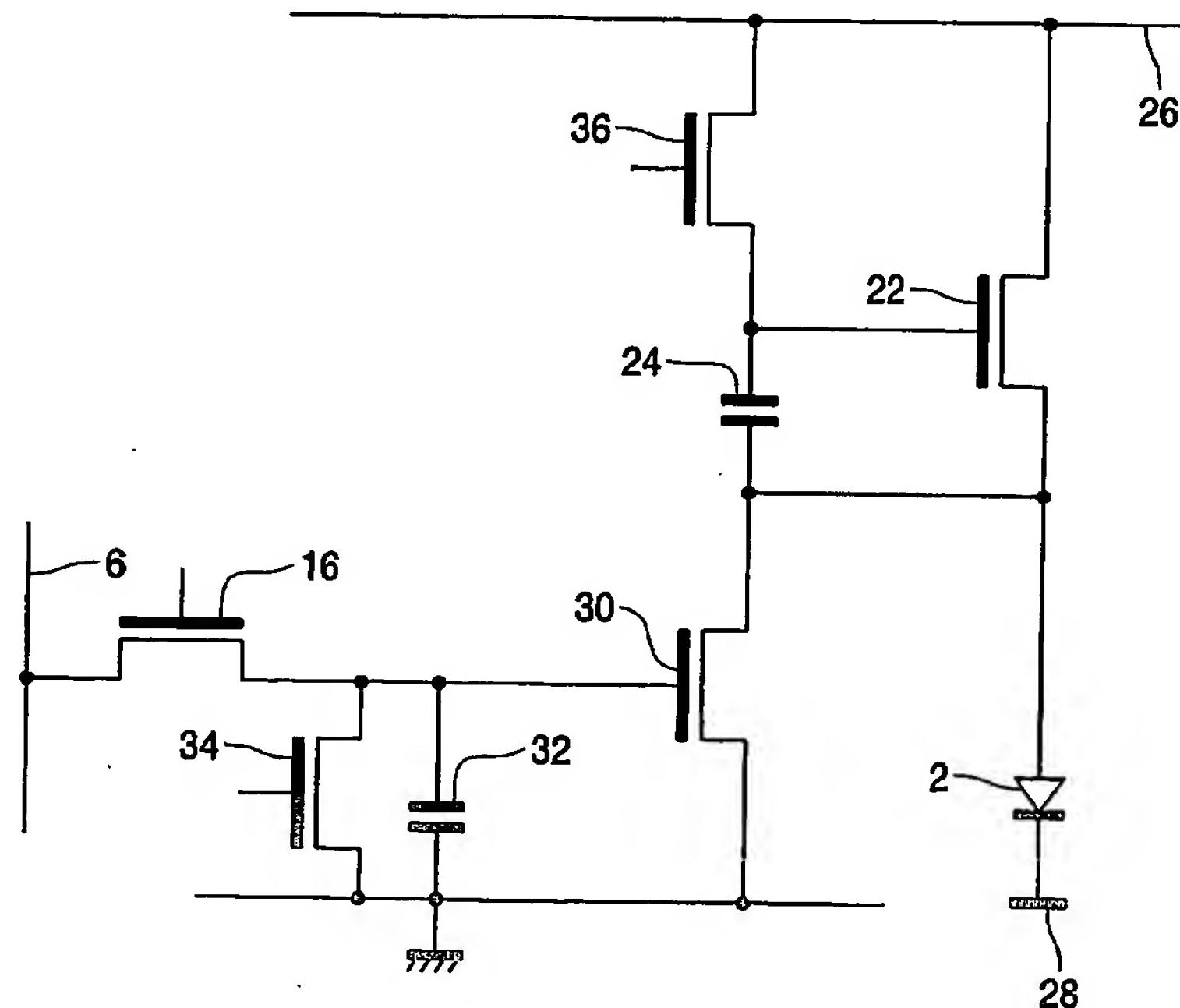
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(54) Title: ACTIVE MATRIX DISPLAY DEVICE



WO 2004/088624 A1

(57) Abstract: An addressing scheme is provided for a display having an array of current driven pixels. An input voltage is used for generating a desired source-drain current using a transistor operated with low duty cycle. This source-drain current is then passed through a drive transistor and the resulting gate-source voltage is stored on a capacitor for subsequent driving of the display element.



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